THE COMMISSIONER OF PATENTS AND TRADEMARKS SHINGTON, D.C. 20231

Enails mitted herewith for filing is the Utility patent application of:

INVENTOR: YUTAKA OGAWA, KAZUTOMO OGURA, NAOFUMI SATOU AND KIYOTADA FUNANE

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Enclosed are:

- 4 sheets of □ formal □ informal drawings.
- ⊠ Request for Priority to claim priority date of November 5, 2002. ⋈ An Executed Declaration.
- ⋈ An assignment executed by inventors to Renesas Technology Corp and Hitachi ULSI Systems Co., Ltd.

□ The filing fee is calculated as shown below:

For	Number Filed		Number Extra	RATE		
TOTAL CLAIMS	17	-20		1	CALCULATION 0.0	
		T	0	x\$18 =		
INDEPENDENT CLAIMS	3	-3	0	x\$84 =	0.00	
MULTIPLE DEPENDENT CLAIM(S) (if applicable) +\$280 =						
		.	BASIC FEE		\$750.00	
			TOTAL OF ABOVE			
EDUCTION BY ½ FOR FILI APPLICABLE, VERIFIED ST	NG BY SMALL FATEMENT ML	ENTITY (note 3 IST BE ATTACHE	7 C.E.R. §§ 1.9, 1.27, 1.28).		750.00	
				TOTAL		

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U	Please charge my Deposit Account Number in the amount		750.00	
	fees. A duplicate conv of this paper in the amount of	f		
	fees. A duplicate copy of this paper is enclosed.	10 (cover the filing and assignment	ent recordation
⊠			0 11-8	ent recordation
	A check in the amount of \$700.00.			

- A check in the amount of \$790.00 to cover the filing fee and assignment fee is enclosed. \boxtimes
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to **Deposit Account**

Respectfully submitted,

Stanley P. Fisher

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REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 September 26, 2003